

EAST Search History

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	3531	(711/118,128,135).ccls.	US_PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/07/14 17:46
L2	5	1 and (prevent\$3 prohibit\$4 inhibit\$3 forbid\$4) same (evict\$3 flush\$3 (writ\$3 near3 back)) same (task micro-operation microoperation operation program code) near5 (switch\$3) and ((hit near5 ratio) (cache near5 hit) (in adj2 cache))	US_PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/07/14 17:46
L3	1	2 and ((control\$4 near3 register) controller) same (enabl\$3 activat\$3) same ((way set) with cache)	US_PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/07/14 17:47
S16	1	(control near3 register) same (activat\$4 set\$3) with (way) same (evict\$3 flush\$3) near3 cache with hit	US_PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/07/11 17:56
S17	1	(control near3 register) same (activat\$4 set\$3) with (way) same (evict\$3 flush\$3) near5 cache with hit	US_PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/07/11 17:57
S18	1	(control near3 register) same (activat\$4 set\$3) with (way) same (evict\$3 flush\$3) with cache with hit	US_PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/07/11 17:57
S19	1	(control near5 register) same (activat\$4 set\$3) with (way) same (evict\$3 flush\$3) with cache with hit	US_PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/07/11 17:59

S20	1	(control near5 register) same (activat\$4 set\$3) with (way) same (evict\$3 flush\$3) same cache same hit	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/07/11 17:59
S21	9	(control near5 register) same (enabl\$3 activat\$4 set \$3) with (way set) same (evict\$3 flush\$3) same cache same hit	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/07/11 18:17
S22	4	(control near5 register) same (enabl\$3 activat\$4 set \$3) with (way set) same (evict\$3 flush\$3) same cache same hit same (task micro-operation microoperation operation)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/07/11 18:28
S23	6	(prevent\$3 prohibit\$4 inhibit\$3) near5 (evict\$3 flush\$3 (writ\$3 near3 back)) same (enabl\$3 activat\$4 set\$3) with (way set) same (evict\$3 flush\$3) same cache same hit	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/07/11 18:40
S24	8	(prevent\$3 prohibit\$4 inhibit\$3) near5 (evict\$3 flush\$3 (writ\$3 near3 back)) same (enabl\$3 activat\$4 set\$3) with (way set) same (evict\$3 flush\$3 (writ\$3 near3 back)) same cache same hit	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/07/11 18:40
S25	1	(control near5 register) same (enabl\$3 activat\$4) with (way set) same (evict \$3 flush\$3) same cache same hit same (task micro- operation microoperation operation)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/07/11 19:12
S26	2	(control near5 register) same (enabl\$3 activat\$4) with (way set) same (evict \$3 flush\$3) same cache same (hit (in adj2 cache)) same (task micro-operation microoperation operation)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/07/11 19:13

S27	1	(prevent\$3 prohibit\$4 inhibit\$3) with (evict\$3 flush\$3 (writ\$3 near3 back)) same (enabl\$3 activat\$4) with (way set) same (evict\$3 flush\$3 (writ \$3 near3 back)) same cache same (hit (in adj2 cache)) same (task micro-operation microoperation operation)	US-PGPUB; USPAT; USOOR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/07/11 19:15
S28	1	(prevent\$3 prohibit\$4 inhibit\$3) with (evict\$3 flush\$3 (writ\$3 near3 back)) same (enabl\$3 activat\$4) same (way set) same (evict\$3 flush\$3 (writ \$3 near3 back)) same cache same (hit (in adj2 cache)) same (task micro-operation microoperation operation)	US-PGPUB; USPAT; USOOR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/07/11 19:16
S29	11	(prevent\$3 prohibit\$4 inhibit\$3) same (evict\$3 flush\$3 (writ\$3 near3 back)) same (enabl\$3 activat\$4) same (way set) same (evict\$3 flush\$3 (writ \$3 near3 back)) same cache same (hit (in adj2 cache)) same (task micro-operation microoperation operation)	US-PGPUB; USPAT; USOOR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/07/11 19:17
S30	1	(prevent\$3 prohibit\$4 inhibit\$3) with (evict\$3 flush\$3 (writ\$3 near3 back)) same (enabl\$3 activat\$4) same (way set) same (evict\$3 flush\$3 (writ \$3 near3 back)) same cache same (hit (in adj2 cache)) same (task micro-operation microoperation operation)	US-PGPUB; USPAT; USOOR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/07/11 19:23
S31	1744	(control near3 register) with (set activat\$3) with (way set) with (processor)	US-PGPUB; USPAT; USOOR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/07/11 19:26

S32	1744	S31 same (control near3 register) with (set activat\$3) with (way set) with (processor)	US-PGPUB; USPAT; USOOR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/07/11 19:27
S33	3	S31 same (prevent\$3 prohibit\$4 inhibit\$3) with (evict\$3 flush\$3 (writ\$3 near3 back))	US-PGPUB; USPAT; USOOR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/07/11 19:27
S34	162	(control near3 register) with (enabl\$3 activat\$3) with (way set) with (processor)	US-PGPUB; USPAT; USOOR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/07/11 19:29
S35	1	S34 same (prevent\$3 prohibit\$4 inhibit\$3) with (evict\$3 flush\$3 (writ\$3 near3 back))	US-PGPUB; USPAT; USOOR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/07/11 19:29
S36	2	S34 and (prevent\$3 prohibit\$4 inhibit\$3) with (evict\$3 flush\$3 (writ\$3 near3 back))	US-PGPUB; USPAT; USOOR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/07/11 19:29
S37	8	(control near3 register) same (enabl\$3 activat\$3) with (cache near5 (way set)) with (processor)	US-PGPUB; USPAT; USOOR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/07/11 19:31
S38	1	S37 and (prevent\$3 prohibit\$4 inhibit\$3) with (evict\$3 flush\$3 (writ\$3 near3 back))	US-PGPUB; USPAT; USOOR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/07/11 19:31
S39	1	S37 and (prevent\$3 prohibit\$4 inhibit\$3) same (evict\$3 flush\$3 (writ\$3 near3 back))	US-PGPUB; USPAT; USOOR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/07/11 19:31

S40	99889	(prevent\$3 prohibit\$4 inhibit\$3) same (evict\$3 flush\$3 (writ\$3 near3 back))	US_PGPUB; USPAT; USOOR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/07/11 19:32
S41	78	(prevent\$3 prohibit\$4 inhibit\$3) same (evict\$3 flush\$3 (writ\$3 near3 back)) same cache same (hit (in adj2 cache)) same (task micro-operation microoperation operation)	US_PGPUB; USPAT; USOOR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/07/11 19:32
S42	1	S41 and (control near3 register) same (enabl\$3 activat\$3) with (cache near5 (way set)) with (processor)	US_PGPUB; USPAT; USOOR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/07/11 19:33
S43	1	S41 and (control near3 register) same (enabl\$3 activat\$3) same (cache near5 (way set)) same (processor)	US_PGPUB; USPAT; USOOR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/07/11 19:34
S44	1	S41 and (control near5 register) same (enabl\$3 activat\$3) same (cache near5 (way set)) same (processor)	US_PGPUB; USPAT; USOOR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/07/11 19:34
S45	4	S41 and (enabl\$3 activat\$3) same (cache near5 (way set)) same (processor)	US_PGPUB; USPAT; USOOR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/07/11 19:34
S46	65	(control near3 register) with (enabl\$3 activat\$3) with ((way set) near5 cache)	US_PGPUB; USPAT; USOOR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/07/14 10:20
S47	11	(control near3 register) with (enabl\$3 activat\$3) with ((way set) near5 cache) with (processor cpu)	US_PGPUB; USPAT; USOOR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/07/14 10:21

S48	1	S47 and (prevent\$3 prohibit \$4 inhibit\$3 forbid\$4) with (evict\$3 flush\$3 (writ\$3 near3 back)) same cache same (hit (in adj2 cache)) same (task micro-operation microoperation operation)	US-PGPUB; USPAT; USOOR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/07/14 10:31
S49	1	S47 and (prevent\$3 prohibit \$4 inhibit\$3 forbid\$4) same (evict\$3 flush\$3 (writ\$3 near3 back)) same cache same (hit (in adj2 cache)) same (task micro-operation microoperation operation)	US-PGPUB; USPAT; USOOR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/07/14 10:33
S50	1	S47 and (prevent\$3 prohibit \$4 inhibit\$3 forbid\$4) same (evict\$3 flush\$3 (writ\$3 near3 back))	US-PGPUB; USPAT; USOOR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/07/14 10:33
S51	31	(control near3 register) with (enabl\$3 activat\$3) with ((way set) near5 cache) same (processor cpu)	US-PGPUB; USPAT; USOOR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/07/14 10:34
S52	1	S51 and (prevent\$3 prohibit \$4 inhibit\$3 forbid\$4) same (evict\$3 flush\$3 (writ\$3 near3 back)) same cache same (hit (in adj2 cache)) same (task micro-operation microoperation operation)	US-PGPUB; USPAT; USOOR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/07/14 10:34
S53	1	S51 and (prevent\$3 prohibit \$4 inhibit\$3 forbid\$4) same (evict\$3 flush\$3 (writ\$3 near3 back))	US-PGPUB; USPAT; USOOR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/07/14 10:34
S54	79	(prevent\$3 prohibit\$4 inhibit\$3 forbid\$4) same (evict\$3 flush\$3 (writ\$3 near3 back)) same cache same (hit (in adj2 cache)) same (task micro-operation microoperation operation)	US-PGPUB; USPAT; USOOR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/07/14 10:35
S55	1	S54 and (control near3 register) with (enabl\$3 activat\$3) with ((way set) near5 cache) same (processor cpu)	US-PGPUB; USPAT; USOOR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/07/14 10:35

S56	92	(prevent\$3 prohibit\$4 inhibit\$3 forbid\$4) same (evict\$3 flush\$3 (writ\$3 near3 back)) same cache same (hit (in adj2 cache)) same (task micro-operation microoperation operation program code)	US_PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/07/14 10:36
S57	1	(prevent\$3 prohibit\$4 inhibit\$3 forbid\$4) same (evict\$3 flush\$3 (writ\$3 near3 back)) same cache same (hit (in adj2 cache)) same (task micro-operation microoperation operation program code) near5 (switch\$3)	US_PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/07/14 10:37
S58	821	(prevent\$3 prohibit\$4 inhibit\$3 forbid\$4) same (evict\$3 flush\$3 (writ\$3 near3 back)) same (task micro-operation microoperation operation program code) near5 (switch\$3)	US_PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/07/14 10:39
S59	1	S58 same (prevent\$3 prohibit\$4 inhibit\$3 forbid\$4) same cache same (hit (in adj2 cache)) same (task micro-operation microoperation operation program code) near5 (switch\$3)	US_PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/07/14 10:40
S60	1	S58 and (prevent\$3 prohibit\$4 inhibit\$3 forbid\$4) same cache same (hit (in adj2 cache)) same (task micro-operation microoperation operation program code) near5 (switch\$3)	US_PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/07/14 10:40
S61	1	(prevent\$3 prohibit\$4 inhibit\$3 forbid\$4) same (evict\$3 flush\$3 (writ\$3 near3 back)) same (task micro-operation microoperation operation program code) near5 (switch\$3) same ((hit near3 ratio) (in adj2 cache))	US_PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/07/14 10:42

S62	1	(prevent\$3 prohibit\$4 inhibit\$3 forbid\$4) same (evict\$3 flush\$3 (writ\$3 near3 back)) same (task micro-operation microoperation operation program code) near5 (switch\$3) same ((hit near5 ratio) (cache near5 hit) (in adj2 cache))	US_PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/07/14 10:44
S63	19	(prevent\$3 prohibit\$4 inhibit\$3 forbid\$4) same (evict\$3 flush\$3 (writ\$3 near3 back)) same (task micro-operation microoperation operation program code) near5 (switch\$3) and ((hit near5 ratio) (cache near5 hit) (in adj2 cache))	US_PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/07/14 10:44
S64	1	S63 and (control near3 register) with (enabl\$3 activat\$3) with ((way set) near5 cache) same (processor cpu)	US_PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/07/14 10:45
S65	1	S63 and (control near3 register) with (enabl\$3 activat\$3) with ((way set) near5 cache)	US_PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/07/14 10:46
S66	1	S63 and (control near3 register) with (enabl\$3 activat\$3) with ((way set) with cache)	US_PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/07/14 10:46
S67	1	S63 and (control near3 register) same (enabl\$3 activat\$3) same ((way set) with cache)	US_PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/07/14 10:46
S68	8	S63 and ((control\$4 near3 register) controller) same (enabl\$3 activat\$3) same ((way set) with cache)	US_PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/07/14 10:47

S69	8	S63 and ((control\$4 near3 register) controller) same (enabl\$3 activat\$3) same ((way set) with cache) same (processor cpu)	US-PGPUB; USPAT; USOOR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/07/14 10:47
S70	11	S63 not S69	US-PGPUB; USPAT; USOOR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/07/14 10:55
S71	20	(prevent\$3 prohibit\$4 inhibit\$3 forbid\$4 ("not" adj3 (allow\$3 permit\$4))) same ((cache near5 (victim \$7 replac\$5)) evict\$3 flush \$3 (writ\$3 near3 back)) same (task micro-operation microoperation operation program code) near5 (switch\$3) and ((hit near5 ratio) (cache near5 hit) (in adj2 cache))	US-PGPUB; USPAT; USOOR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/07/14 14:23
S72	8	("20030236947" "6295580" "5845309" "5353425").pn.	US-PGPUB; USPAT; USOOR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/07/14 14:27
S73	9	S71 and ((control\$4 near3 register) controller) same (enabl\$3 activat\$3) same ((way set) with cache)	US-PGPUB; USPAT; USOOR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/07/14 14:28
S74	19	(prevent\$3 prohibit\$4 inhibit\$3 forbid\$4) same (evict\$3 flush\$3 (writ\$3 near3 back)) same (task micro-operation microoperation operation program code) near5 (switch\$3) and ((hit near5 ratio) (cache near5 hit) (in adj2 cache))	US-PGPUB; USPAT; USOOR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/07/14 14:28
S75	8	S74 and ((control\$4 near3 register) controller) same (enabl\$3 activat\$3) same ((way set) with cache)	US-PGPUB; USPAT; USOOR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/07/14 14:28

S76	1	S73 not S75	US_PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/07/14 14:28
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